

#### 1 Scope:

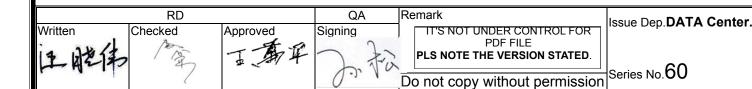
- 1.1 This specification is applicable to lead free and halogen free of RoHS directive for RSH series anti-sulfurated high power thick film chip resistors.
- 1.2 Superior sulfur resistant capability (Refer to ASTM-B-809-95&EIA977 sulfur vapor test).
- 1.3 The product is for general electronic purpose.

### 2 Explanation Of Part Numbers:

					لے	٦
Туре	Size	Packaging	Nom	inal Resistance	Resistance Tolerance	FoS Test
Anti-sulfurated High Power	1206 1210	T : Taping 4 mm Pitch	5% (3- Digit)	EX. 10Ω=100 4.7 =4R7 JUMPER=000	D=± 0.5%	
Thick Film Chip Resistors	2010 2512	Carrier Tape	0.5% 1% (4-Digit)	EX. 10.2Ω=10R2 10KΩ=1002 JUMPER=0000	. F=± 1% J=± 5%	B : 105℃

#### 3 General Specifications:

Type Rated Max. Working Overload Verlear			T.C.R (ppm/ ℃)	(ppm/ Resistance Range ℃)			JUMPER (0Ω) Rated Current		JUMPER (0Ω) Resistance Value			
	70℃	Voltage	Voltage		B(±0.1%) E-24 ∖ E-96	D(±0.5%) E-24 ∖ E-96	F(±1%) E-24 ∖ E-96	J(±5%) E-24	J (±5%)	F (±1%)	J (±5%)	F (±1%)
RSH	$\frac{1}{2}W$	200V	400V	±100		$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	3A	5A	100mΩ	50mΩ
(1206)	$\frac{1}{2}$ VV	2007	4000	±200		$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	34	54	MAX.	MAX.
RSH	3 W	200V	400V	±100		$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}20M\Omega$	4A	6A	100mΩ	50mΩ
(1210)	$\frac{1}{4}$ VV	2007	4000	±200			$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	44	0A	MAX.	MAX.
RSH	1W	200V	400V	±100			$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	4.5A	7A	100mΩ	50mΩ
(2010)	1 V V	2007	4000	±200			$1\Omega{\leq}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	4.5A	78	MAX.	MAX.
RSH	2W	200V	400V	±100		$100\Omega \leq R \leq 100K$	$10\Omega{\leq}R{\leq}10M\Omega$	$10\Omega{\leq}R{\leq}10M\Omega$	6A	10A	100mΩ	50mΩ
(2512)	ZVV	2007	400V	±200			$1\Omega{\le}R{<}10\Omega$	$1\Omega{\leq}R{<}10\Omega$	δA	IUA	MAX.	MAX.
Ope	rating Te	mperature	Range				-55℃ ~+15	5℃				

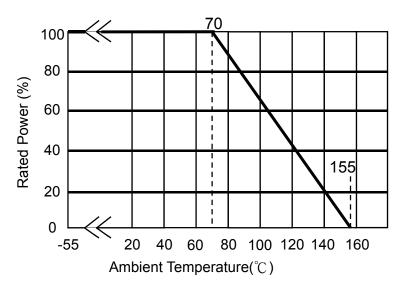




3.1 Power Derating Curve:

Operating Temperature Range:- 55  $\,\sim\,$  155  $^\circ \! \mathbb{C}$ 

If the ambient temperature exceeds 70 degrees centigrade to 155 degrees centigrade, the power can be modified by the curve as below.



#### 3.2 Voltage Rating:

Rated Voltage: DC voltage or AC voltage (rms) based on the rated power. The voltage can be calculated by the following formula. If the calculated value exceeds the Max voltage specified in the Table 3, the Max voltage rating is set as the voltage rating.



E= Rated voltage(V) P= Power rating(W) R= Nominal resistance(Ω)

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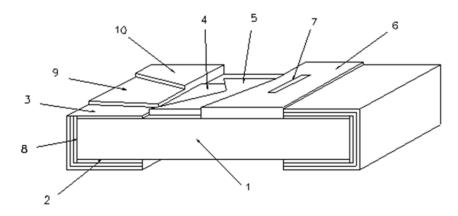
# RSH Series Anti-sulfurated High Power Thick Film Chip Resistors Product Specification

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# 4 Dimensions:

							Unit:mm
2873	Туре	Dimension Size Code	L	W	н	L1	L2
• <b></b> •	RSH	1206	3.05±0.10	1.55±0.10	0.50±0.10	0.45±0.20	0.35±0.15
	RSH	1210	3.05±0.10	2.55±0.10	0.55±0.10	0.50±0.20	0.50±0.20
	RSH	2010	4.95±0.10	2.45±0.10	0.70±0.10	0.55±0.20	0.60±0.20
- L2 - L2	RSH	2512	6.40±0.20	3.20±0.20	0.70±0.10	0.60±0.20	1.25±0.20

# 5 Structure Graph:



1	Ceramic substrate	6	2nd Protective coating
2	Bottom inner electrode	7	Marking
3	Top inner electrode	8	Terminal inner electrode
4	Resistive layer	9	Ni plating
5	1st Protective coating	10	Sn plating

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# 6 Reliability Test:

#### 6.1 Electrical Performance Test

Item	Conditions	Specification	าร
item	Conditions	Resistors	Jumper
Temperature Coefficient of Resistance	TCR (ppm/°C) = $\frac{(R2-R1)}{R1(T2-T1)}$ ×10 <sup>6</sup> R1: Resistance at room temperature R2: Resistance at -55°C or +125°C T1: Room temperature T2: Temperature -55°C or +125°C Refer to JIS-C5201-1 4.8	Refer to item 3. general specifications	NA
Short Time Over load	Applied 2.5 times rated voltage for 5 seconds and release the load for about 30 minutes , then measure its resistance variance rate.(Rated voltage refer to item 3. general specifications) Refer to JIS-C5201-1 4.13	0.5%、1%: ∆R%=±1.0% 5%: ∆R%=±2.0%	Refer to iter 3. general specificatior
Insulation Resistance	Put the resistor in the fixture, add 100 VDC in + ,- terminal for 60 sec then measured the insulation resistance between electrodes and insulating enclosure or between electrodes and base material. Refer to JIS-C5201-1 4.6	≥10 <sup>9</sup> Ω	
Dielectric Withstand Voltage	Put the resistor in the fixture, add VAC (see spec. below) in +,- terminal for. RSH1206,1210,2010,2512 apply 500 VAC 1 minute. Refer to JIS-C5201-1 4.7	No short or burned on the a	opearance.

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Item	Conditions	Specifications	
item	Conditions	Resistors	Jumper
Terminal Strength	<ul> <li>Test1:The resistor mounted on the board ,then applied 5N pushing force on the sample rear for 10sec.</li> <li>Test2:The resistor mounted on the board and slowly add force on the sample rear until the sample termination is breakdown.</li> </ul>		al damage
Resistance Solvent	its resistance variance rate. Refer to JIS-C5201-1 4.29	∆R%=±0.5%	Refer to ite 3. general specificatio
Solderabili	The resistor be immersed into solder pot in temperature 235±3℃ for 2±0.5 sec, then the resistor is left as placed under microscope to observed its solder area.	Solder coverage over 95%	
Resistance Soldering H	of $260+5/-0^{\circ}$ for $30+1/-0$ seconds. Then the	<ul> <li>Test item 1:</li> <li>(1).Variance rate on resistance ΔR%=±1.0%</li> <li>Test item 2:</li> <li>(1).Solder coverage over 95%.</li> <li>(2).The underlying material (such as ceramic) shall not be visible at the crest corner area of the electrode.</li> <li>Test item 3:</li> <li>(1).Variance rate on resistance ΔR%=±1.0%</li> </ul>	Refer to ite 3. general specificatio
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	•				
	Itom	Conditions		Specifications	
	Item		Resis	stors	Jumper
		◎Bending Strength:	∆R%=±1.0%		Refer to item
		Solder tested resistor on to PC board. Add force in			<ol><li>general</li></ol>
		the middle down, and under load measured its			specification
		resistance variance rate.			
		D:RSH1206 \ 1210=3mm			
		RSH2010 \ 2512=2mm			
		Resistor Testing circuit boord			
Joint	t Strength	45 45			
of	Solder	la da / d			
•••		Chip resistor			
		وال الشاخير			
		Pressurtze			
		D (Arrount of band)			
		OHM Meter			
		Refer to JIS-C5201-1 4.33			
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#### 6.3 Environmental Test

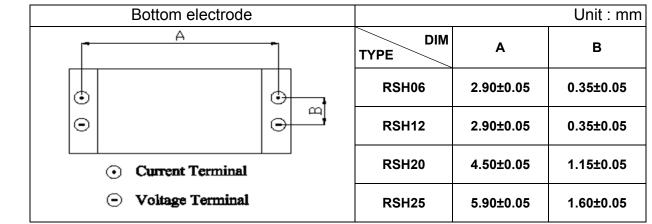
						Specifications	
	Item		Conditions	S	Resi	stors	Jumper
	istance to ry Heat	155±5℃ tested res and meas	d resistor in chamber for 1000 +48/-0 hours sistor in room tempera sure its resistance var	s. Then leaving the ature for 60 minutes,	0.5%、1%: ∆R%=±1.0% 5%: ∆R%=±2.0%		Refer to item 3. general specifications
		Put chip i the tempe $^{\circ}$ C for 15	JIS-C5201-1 4.25 resistors in the therma erature was -55°C for minutes, the total of 3 , let stand for more tha	15 minutes and +125 300 times and then			Refer to item 3. general specifications
Therr	mal Shock	measurin	g the resistance chan Testing Cond vest Temperature	ge rate ition -55±5℃			
		Tempe Refer to I	hest Temperature rature-retaining time MIL-STD 202 Method		0.5%		Defende iter
	ading Life Moisture	temperation load the roff, total f in room to resistanc	ested resistor in the ch ure 40±2°C, relative he rated voltage for 90 m 1000 hours. Then leav emperature for 60 min e variance rate JIS-C5201-1 4.24	umidity 90~95% and inutes on, 30 minutes ring the tested resistor	∆R%=±3.0%		Refer to item 3. general specification
Lo	oad Life	Put the te 70±2℃ a on, 30 mi the tested	ested resistor in chamb and load the rated volt nutes off, total 1000 h d resistor in room tem and measure its resis	age for 90 minutes ours. Then leaving perature for 60	0.5%		Refer to ite 3.general specification
Sulfu	ration Test		JIS-C5201-1 4.25 Put the tested resisto temperature of 105±2 Refer to ASTM-B-809		∆R=±4.0%		Refer to ite 3.general specification
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#### 7 Measurement Point :



## 8 Plating Thickness :

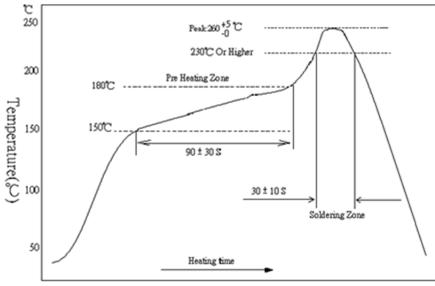
- 8.1 Ni: $\geq$ 2 $\mu$ m
- 8.2 Sn(Tin):≧3µm
- 8.3 Sn(Tin):Matte Sn

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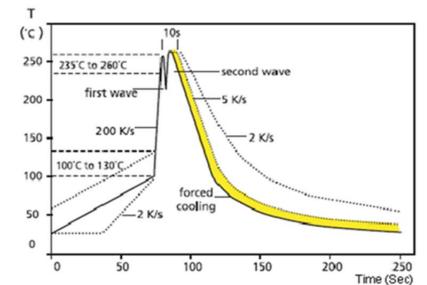
# 9 Technical application notes:(This is a recommendation ,please adjust it according to actual application)

- 9.1 Recommend Soldering Method:
  - 9.1.1 Lead Free IR Reflow Soldering Profile



Remark1:Recommended IR Reflow Soldering Profile meet J-STD-020D. Remark2:The peak temperature of soldering heat is  $260 + 5/-0^{\circ}$  for 10 seconds

9.1.2 Lead Free Double-Wave Soldering Profile(Applicable to products above 0603(inclusive))



9.1.3 Soldering Iron: temperature  $350^{\circ}$ C ±10°C, dwell time shall be less than 3 sec.

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9.2 Land Pattern Design (For Reflow Soldering) :

When a component is soldered, the resistance after soldering changes slightly depending on the size of the soldering area and the amount of soldering. When designing a circuit, it is necessary to consider the effect of a decrease or increase in its resistance.

				Unit:mm
	DIM	А	В	С
	RSH06	2.2	4.2	1.6
A	RSH12	2.2	4.2	2.8
	RSH20	3.5	6.1	2.8
- B	RSH25	3.8	8.0	3.5

9.3 Environment Precautions:

This specification product is for general electronic use, RALEC will not be responsible for any damage, cost or loss caused by using this specification product in any special environment. If other applications, you need to confirm with RALEC.

If consumer intends to use our Company product in special environment or condition (including but not limited to those mentioned below), then will need to make individual recognition of product features and reliability accordingly.

- (a) Used in high temperature and humidity environment.
- (b) Exposed to sea breeze or other corrosive gas, such as Cl2  $\smallsetminus$  H2S  $\smallsetminus$  NH3  $\smallsetminus$  SO2 and NO2.
- (c) Used in non-verified liquids including water, oil, chemical and organic solvents.
- (d) Using non-verified resin or other coating material to seal or coat our Company product.
- (e) After soldering, it is necessary to use water-soluble detergents to clean residual solder fluxes, even though no-clean fluxes are recommended.

#### 9.4 Momentary Overload Precautions:

The product might be out of function when momentary overloaded. Please make sure to avoid momentary overloading while using and preserving.

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- 9.5 Operation and Processing Precautions:
  - (a) Avoid damage to the edge of resistor and protective layer caused by mechanical stress.
  - (b) Handle with care when printing circuit board (PCB) is divided or fixed on support body, because bending of printing circuit board (PCB) mounting will make mechanical stress for resistors.
  - (c) Make sure the power rating is under the limit when using the resistor. When power rating is over the limit, the resister will be overloaded. There might be machinery damage due to the climbing temperature.
  - (d) If the resister will be exposed under massive impact load (shock wave) in a short period of time, the working environment must be set up well before use.
  - (e) Please make evaluation and confirmation when the product is well used in your company and have a through consideration of it's fail-safe design to ensure the system safety.

#### 10 Stock period:

- 10.1 The temperature condition must be controlled as 25±5℃, the R.H. must be controlled as 60±15%. The stock can maintain quality level in two years.
- 10.2 Please avoid the mentioned harsh environment below when storing to ensure product performance and its weldability. Places exposed to sea breeze or other corrosive gas, such as Cl<sub>2</sub> × H<sub>2</sub>S × NH<sub>3</sub> × SO<sub>2</sub> and NO<sub>2</sub>.
- 10.3 When the product is moved and stored, please ensure the correct orientation of the box. Do not drop or squeeze the box. Otherwise, the electrode or the body of the product may be damaged.

# 11 The carton packaged for electronic-information products is made by the symbol as follows: (For china)



#### 12 Attachments:

12.1 Document Revise Record(QA-QR-027)

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